## Notice of References Cited

Application/Control No.	Applicant(s)/Patent Under Reexamination	
09/434,736	KIM ET AL.	
Examiner	Art Unit	Dana 1 of 1
Evan T. Pert	2813	Page 1 of 1

## **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name		Classification	
X	Α	US-5,318,923	06-1994	Park			
X	В	US-5,069,749	12-1991	Gutierrez			
	С	US-4,707,457	11-1987	Erb			
Χ	D	US-5,683,938	11-1997	Kim et al.			
	Ε	US-					
	F	US-					
	G	US-					
	Н	US-					
	_	US-					
	J	US-					
	К	US-					
	L	US-					
	М	US-					

## FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N			•		
	0					
	Р					
	Q					
	R					
	s			_		
	Т					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, SILICON PROCESSING FOR THE VLSI ERA - VOLUME II, 1990, Lattice Press, page 192.
	٧	
	W	
	х	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.